

1 **ABSTRACT**

2 A disk controller includes memory that is accessible by both a
3 microprocessor and hardware parity logic. Parity-related operations are identified
4 by scenario, and parity coefficient subsets are stored in a memory table for each
5 different parity-related calculation scenario. To perform a particular parity-related
6 operation, the microprocessor determines the operation's scenario and identifies
7 the corresponding coefficient subset. The hardware parity logic is then instructed
8 to perform the appropriate parity computation, using the identified coefficient
9 subset.
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